Vishay High Power Products

Schottky Rectifier, 1 A



- · Low profile, axial leaded outline
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free plating
- Designed and qualified for industrial level

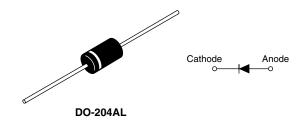
DESCRIPTION

The MBR150, MBR160 axial leaded Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	1.0	A		
V _{RRM}		50/60	V		
I _{FSM}	$t_p = 5 \ \mu s \ sine$	150	A		
V _F	1 Apk, T _J = 125 °C	0.65	V		
TJ	Range	- 40 to 150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	MBR150	MBR160	UNITS	
Maximum DC reverse voltage	V _R	50	60	V	
Maximum working peak reverse voltage	V _{RWM}	50	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 4	I _{F(AV)}	50 % duty cycle at $T_C = 75$ °C, rectangular waveform 1.0		1.0	
Maximum peak one cycle non-repetitive surge current	Irou	5 μs sine or 3 μs rect. pulse	Following any rated load	150	A
See fig. 6	IFSM	10 ms sine or 6 ms rect. pulse	V _{RRM} applied	25	
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \text{ °C}, I_{AS} = 1 \text{ A}, L = 4 \text{ mH}$		2.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by, T _J maximum V _A = 1.5 x V _R typical		1.0	А



PRODUCT SUMMARY			
I _{F(AV)}	1 A		
V _R	50/60 V		
I _{RM}	10 mA at 125 °C		







ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TES	VALUES	UNITS	
Maximum forward voltage drop See fig. 1	V _{FM} ⁽¹⁾	1 A		0.75	v
		2 A	T _J = 25 °C	0.9	
		3 A		1.0	
		1 A		0.65	
		2 A	T _J = 125 °C	0.75	
		3 A		0.82	
	I _{RM} ⁽¹⁾	T _J = 25 °C		0.5	mA
Maximum reverse leakage current See fig. 2		T _J = 100 °C	V _R = Rated V _R	5	
		T _J = 125 °C		10	
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		55	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10			V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation See fig. 4	80	°C/W
			0.33	g
Approximate weight			0.012	oz.
Marking davias			MBR150	
Marking device		Case style DO-204AL (DO-41)	MBR160	

Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

(2) Mounted 1" square PCB, thermal probe connected to lead 2 mm from package



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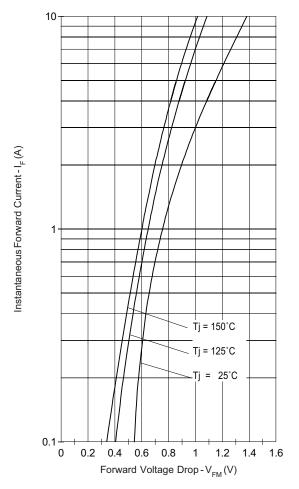
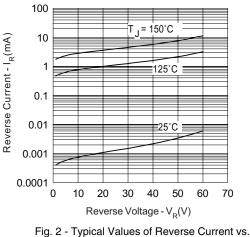


Fig. 1 - Maximum Forward Voltage Drop Characteristics



Reverse Voltage

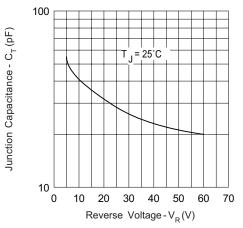


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

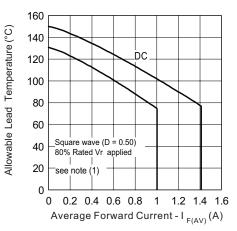
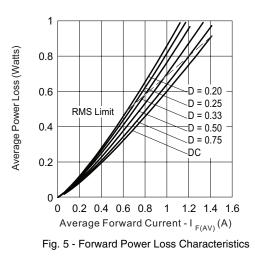


Fig. 4 - Maximum Ambient Temperature vs. Average Forward Current, Printed Circuit Board Mounted



Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = Forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{REV} = Inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = 80 % rated V_R

MBR150, MBR160

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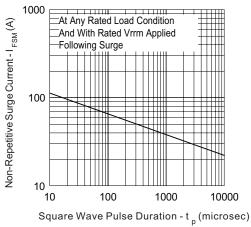
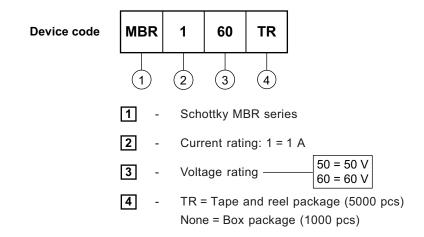


Fig. 6 - Maximum Non-Repetitive Surge Current

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95241				
Part marking information	http://www.vishay.com/doc?95304			
Packaging information	http://www.vishay.com/doc?95308			



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